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T. BELL
8-6-03

Atty. Docket No. 8023-1018

PATENTS

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Mototsugu OKUSHIMA

Confirmation No. 4991

Serial No. 09/852,735

GROUP 2836

Filed May 11, 2001

Examiner Zeev Kitov

ESD PROTECTION APPARATUS AND METHOD
FOR FABRICATING THE SAME

AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Responsive to the Official Action of March 17, 2003,
please amend the above-identified application as follows:

IN THE ABSTRACT:

A substitute Abstract of the Disclosure is provided on
an accompanying separate sheet.

IN THE SPECIFICATION:

Page 1, replace the paragraph, beginning on line 12, as
follows:

41
--A conventional ESD protection apparatus in the CMOS
process generally protects a semiconductor integrated circuit
using a MOSFET transverse parasitic bipolar transistor by
releasing the electric current in the transverse direction
(lateral direction) in a silicon substrate. On the other hand,
the ESD protection apparatus has been required to be further